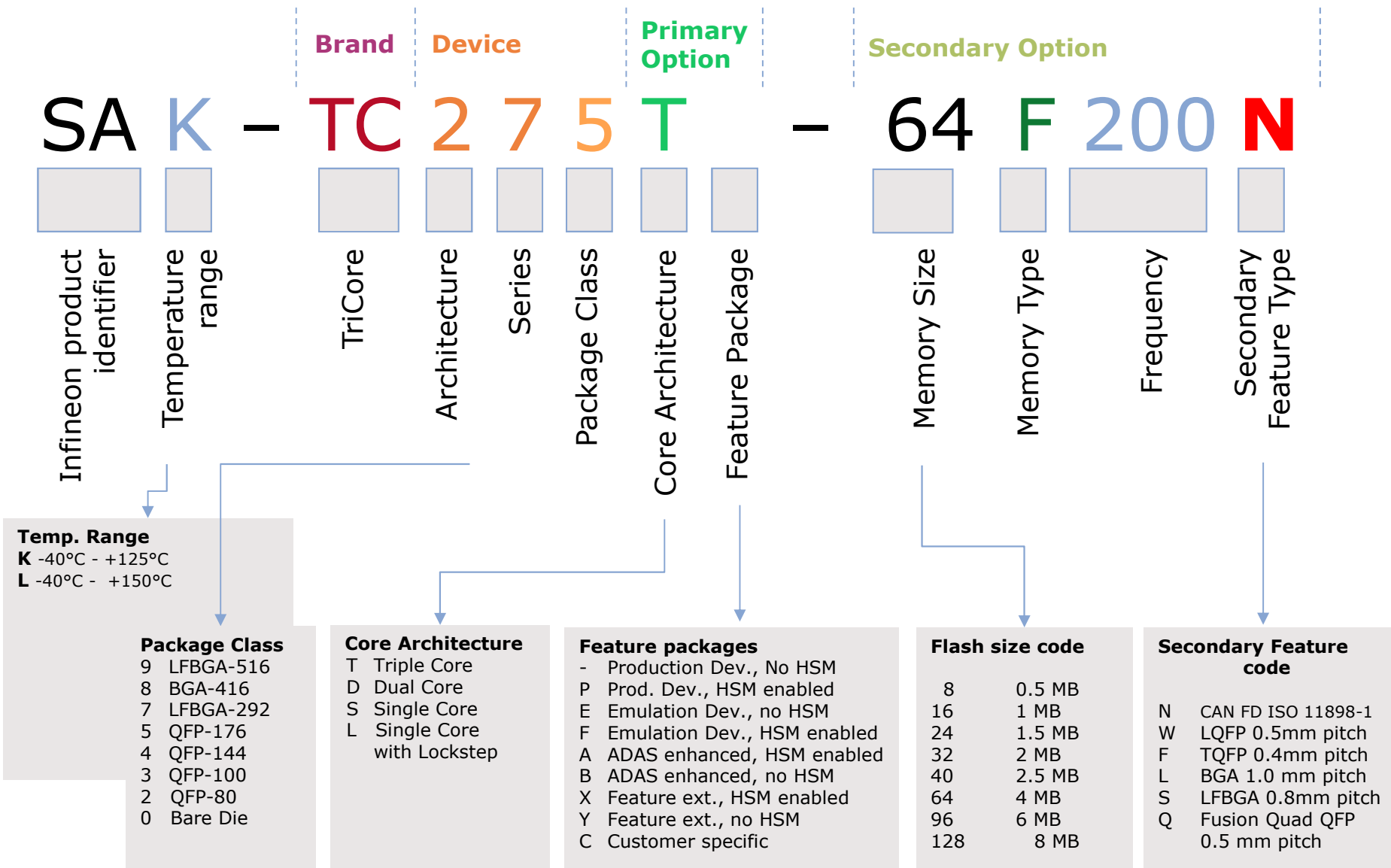


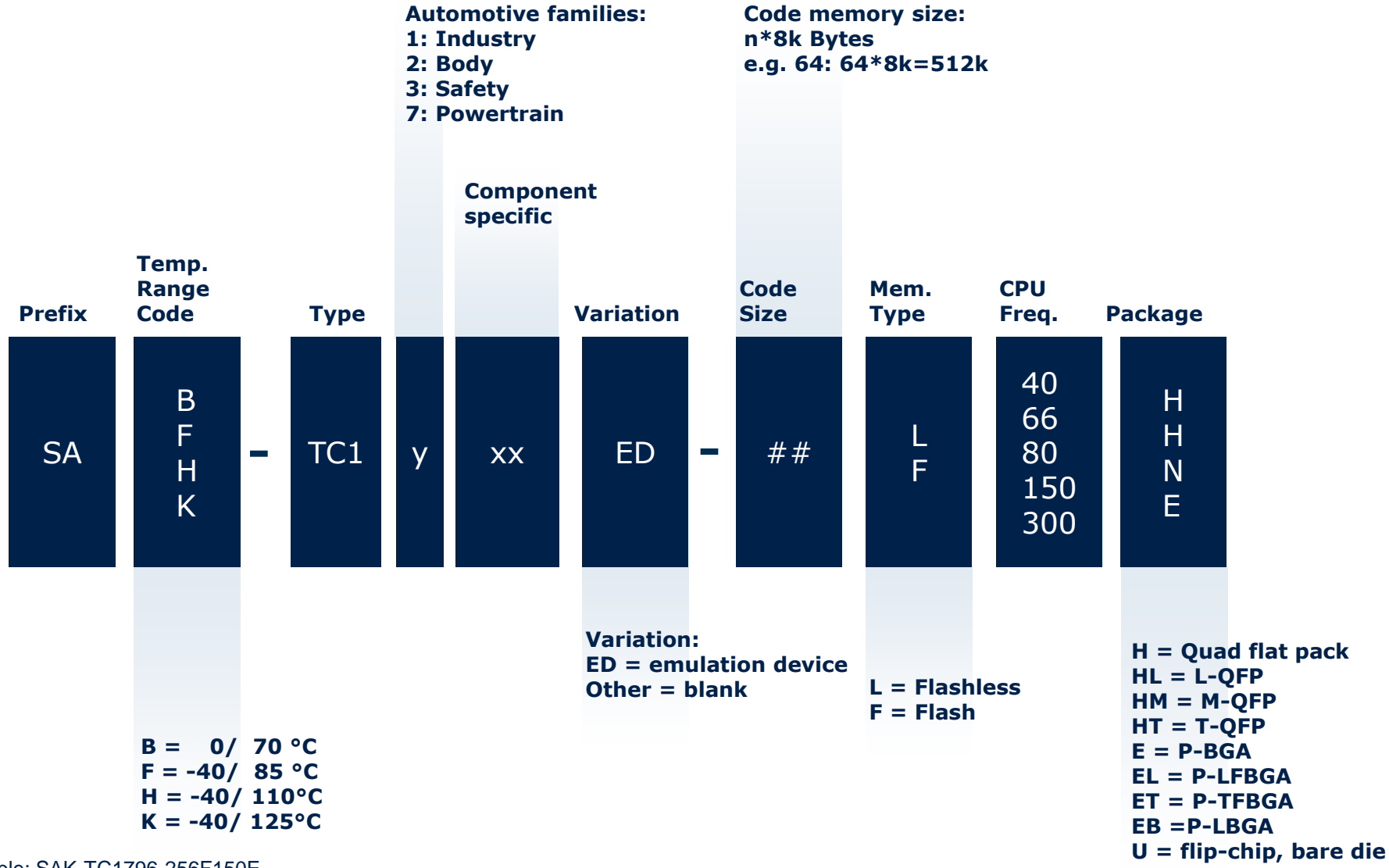
# AURIX™ Product Naming



# Naming Convention for **TriCore®** Family



Example: SAK-TC1796-256F150E



Example: SAK-TC1796-256F150E

# Naming Convention for **XMC4000** Family



Example: XMC4500–F144K1024 AC

Family	Core	Feature			Package	Pins	Temp.	Flash [kB]	Step
XMC	4	5	0	0	E F Q	144 100 64 48	K X F	1024	AA AB AC
		4		2				768	
		2		4				512	
		1		8				256	
								128	
								64	

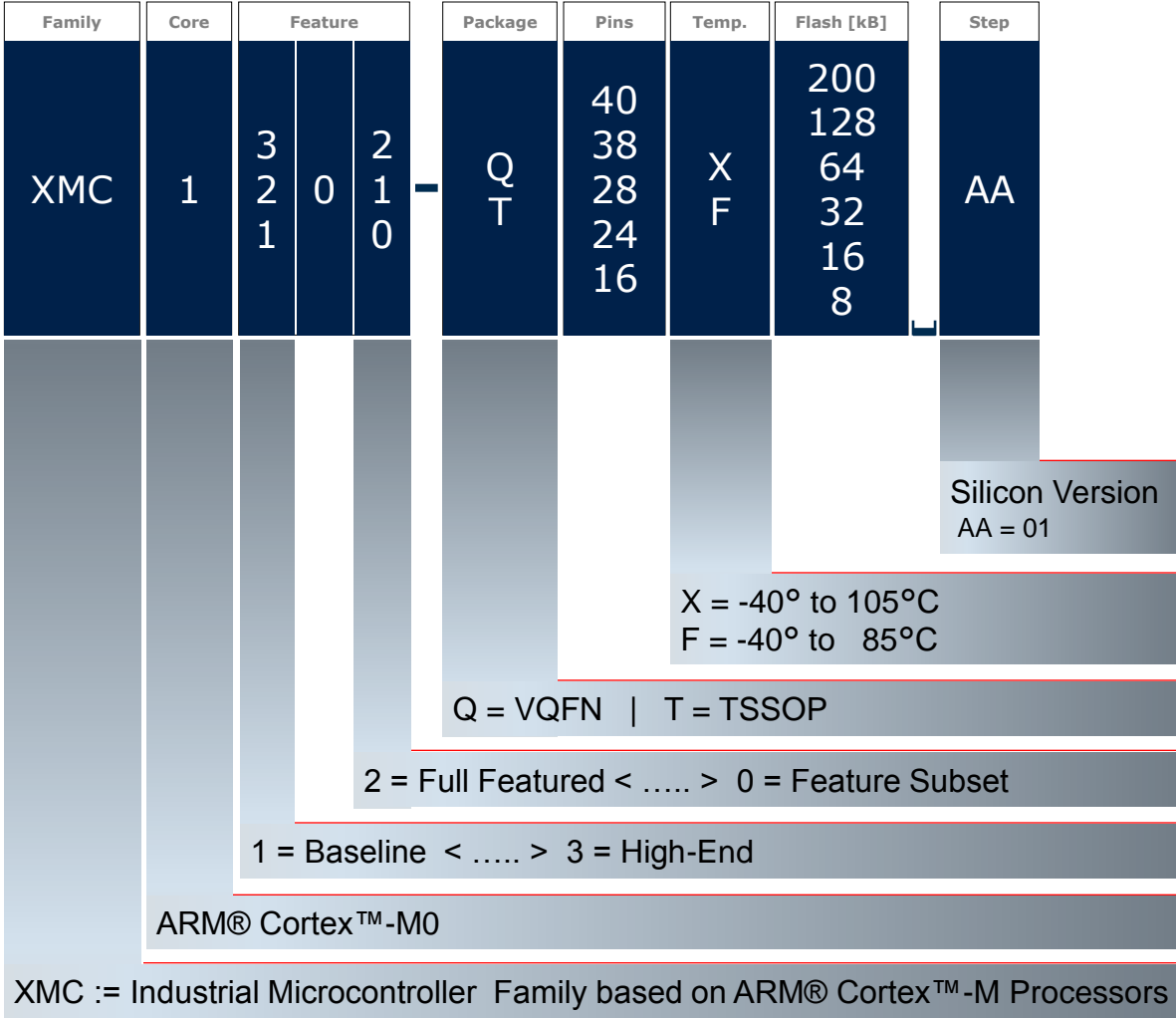
  

<p>Silicon Version AA = 01 AB = 02 AC = 03</p> <p>K = -40° to 125°C X = -40° to 105°C F = -40° to 85°C</p> <p>E = LFBGA   F = LQFP   Q = VQFN</p> <p>0 = Full Featured &lt; ..... &gt; 8 = Feature Subset</p> <p>1 = Baseline &lt; ..... &gt; 5 = High-End</p> <p>ARM® Cortex™-M4 with built in DSP, SPFPU, DMA and MPU</p> <p>XMC := Industrial Microcontroller Family based on ARM® Cortex™-M Processors</p>
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# Naming Convention for **XMC1000** Family



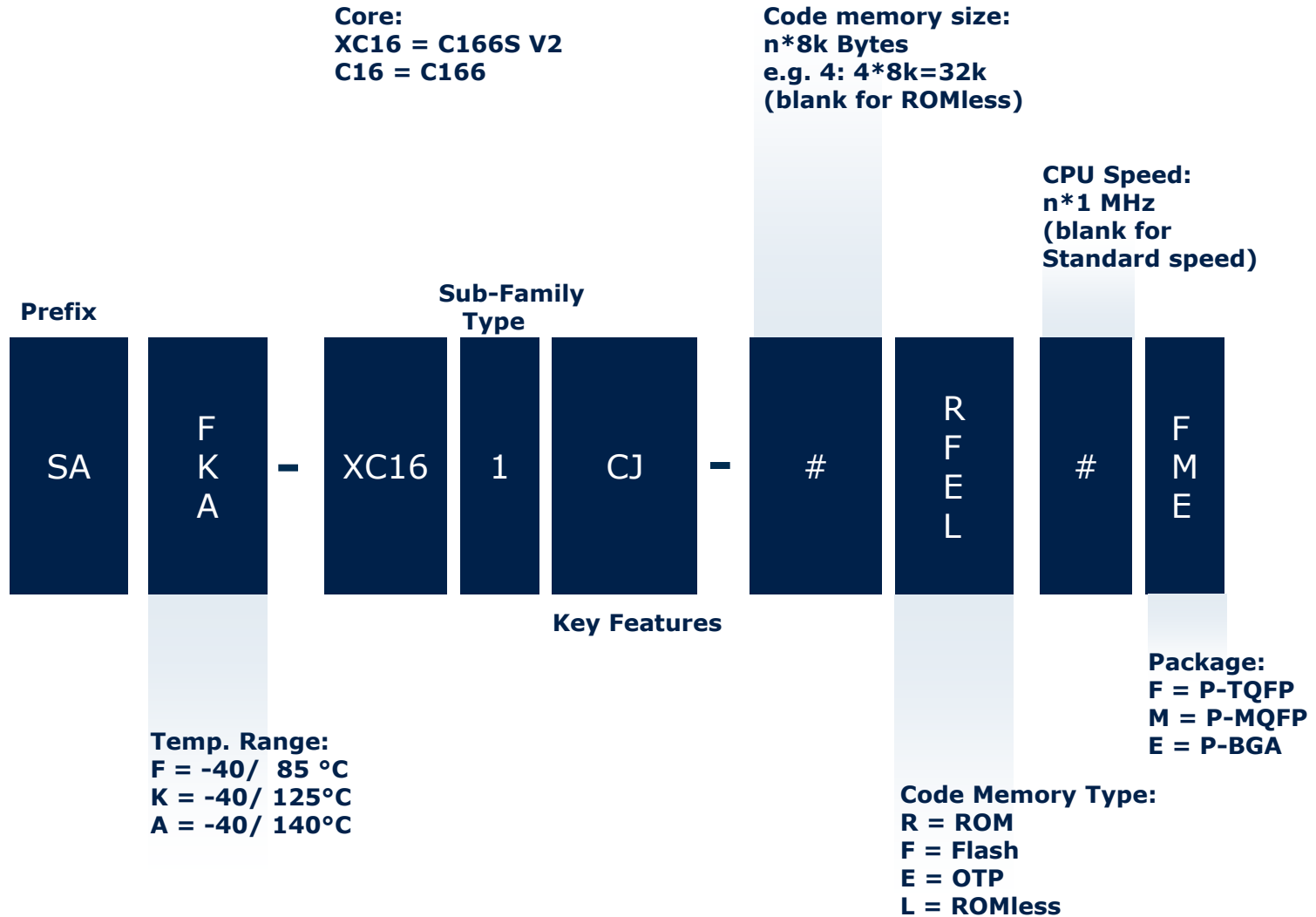
Example: XMC1302-T038X0200 AA



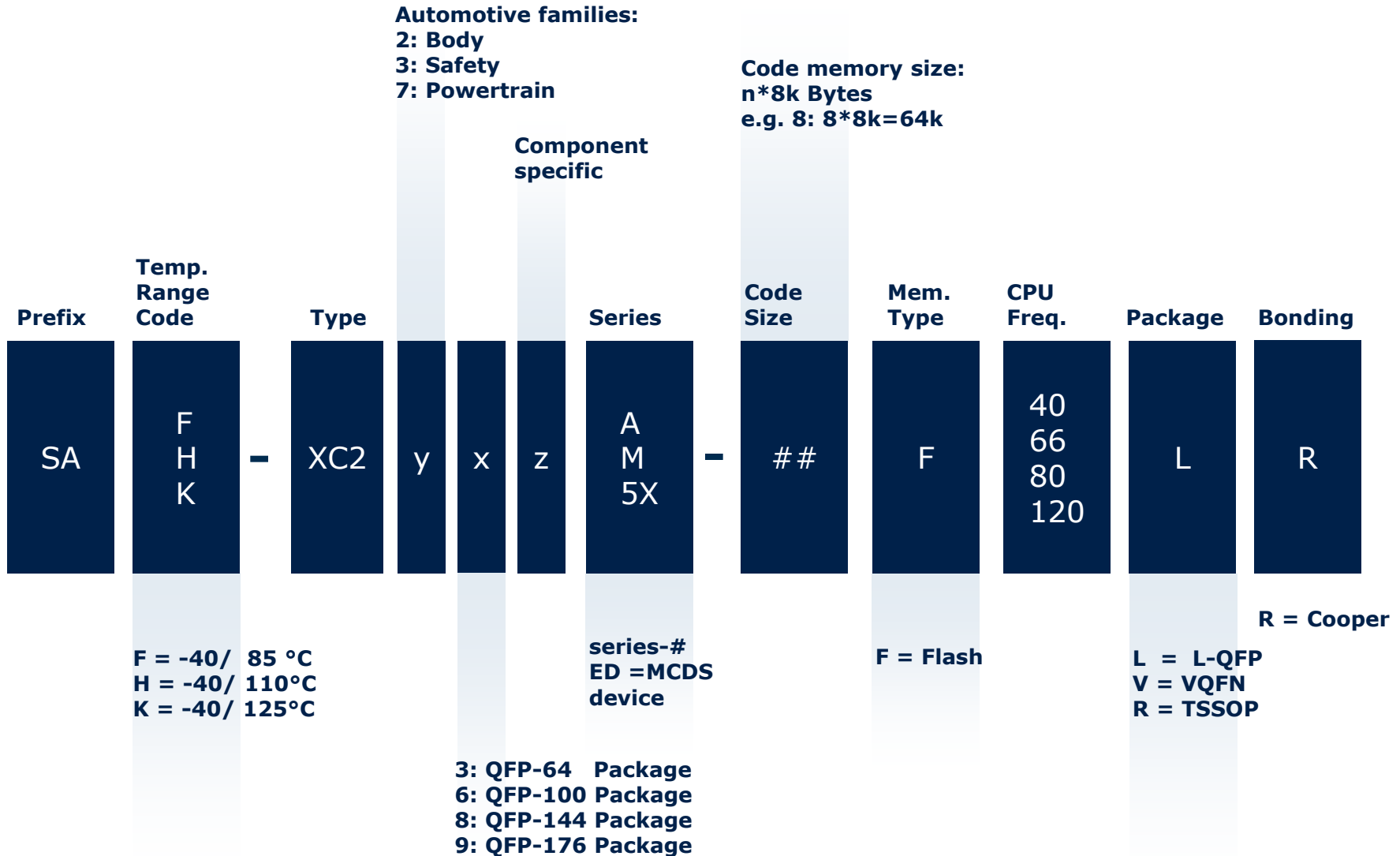
# Naming Convention for **16-bit** Family



Example: SAF-XC161CJ-14F20F



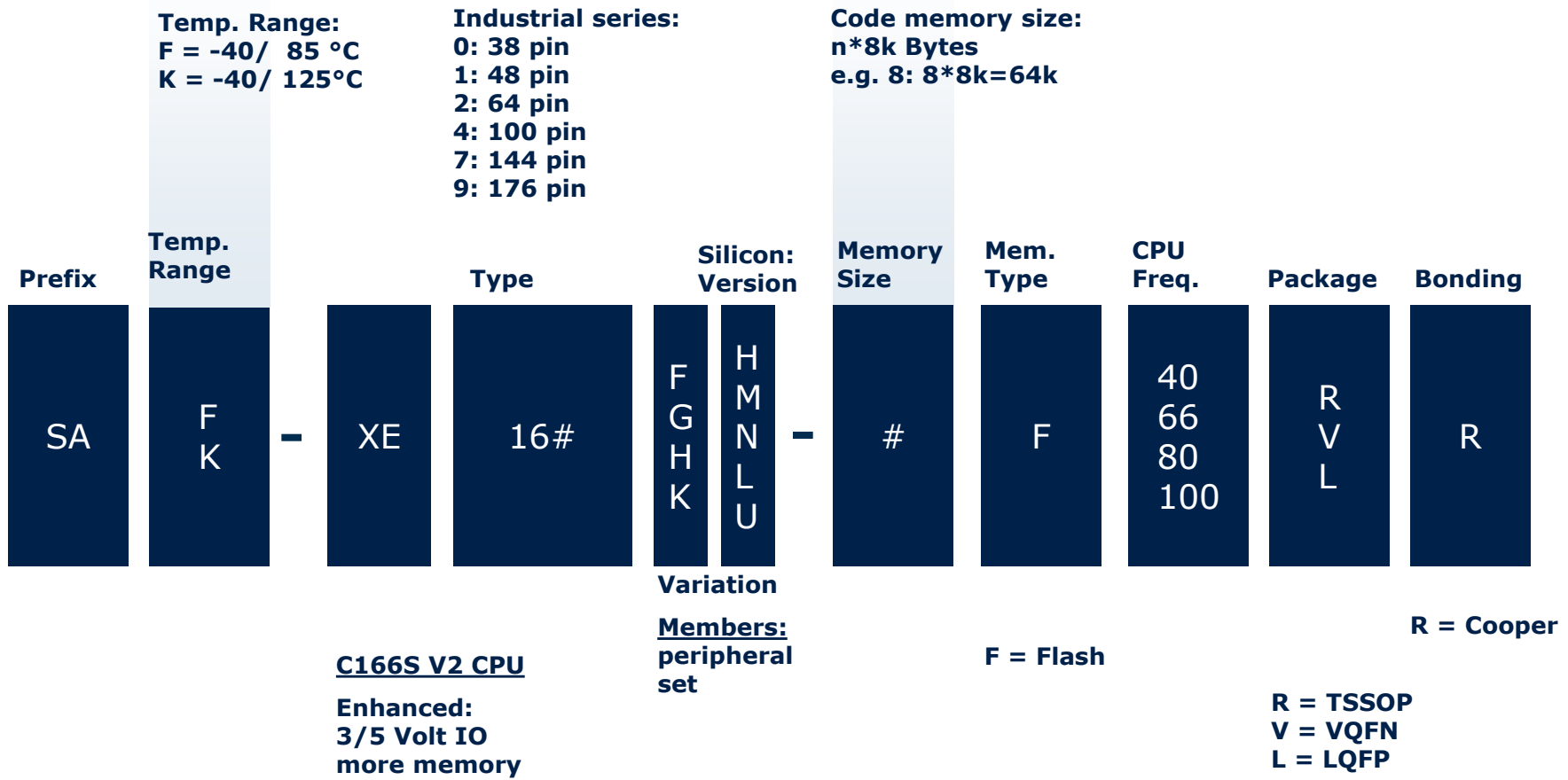
# Naming Convention for **XC2000** Family



# Naming Convention for XE166 Family



Example: SAF-XE167F-96F80L



# Naming Convention for **XC800** Family



**XC800 Family product**  
+  
**2 digits: component specific**

**Code memory size:**  
**n\*4k Bytes**  
e.g. 4: 4\*4k=16k

**5V = 5V Supply**  
**3V3 = 3V Supply**

**F = Flash**  
**R = ROM**



**F = -40°C-85°C**  
**X = -40°C-105°C**  
**K = -40°C-125°C**  
**A = -40°C-140°C**  
**L = -40°C-150°C**

**C = CAN**  
**L = LIN**  
**M = MDU**

**R = TSSOP**  
**F = T/LQFP**  
**G = P-DSO**  
**V = VQFN**

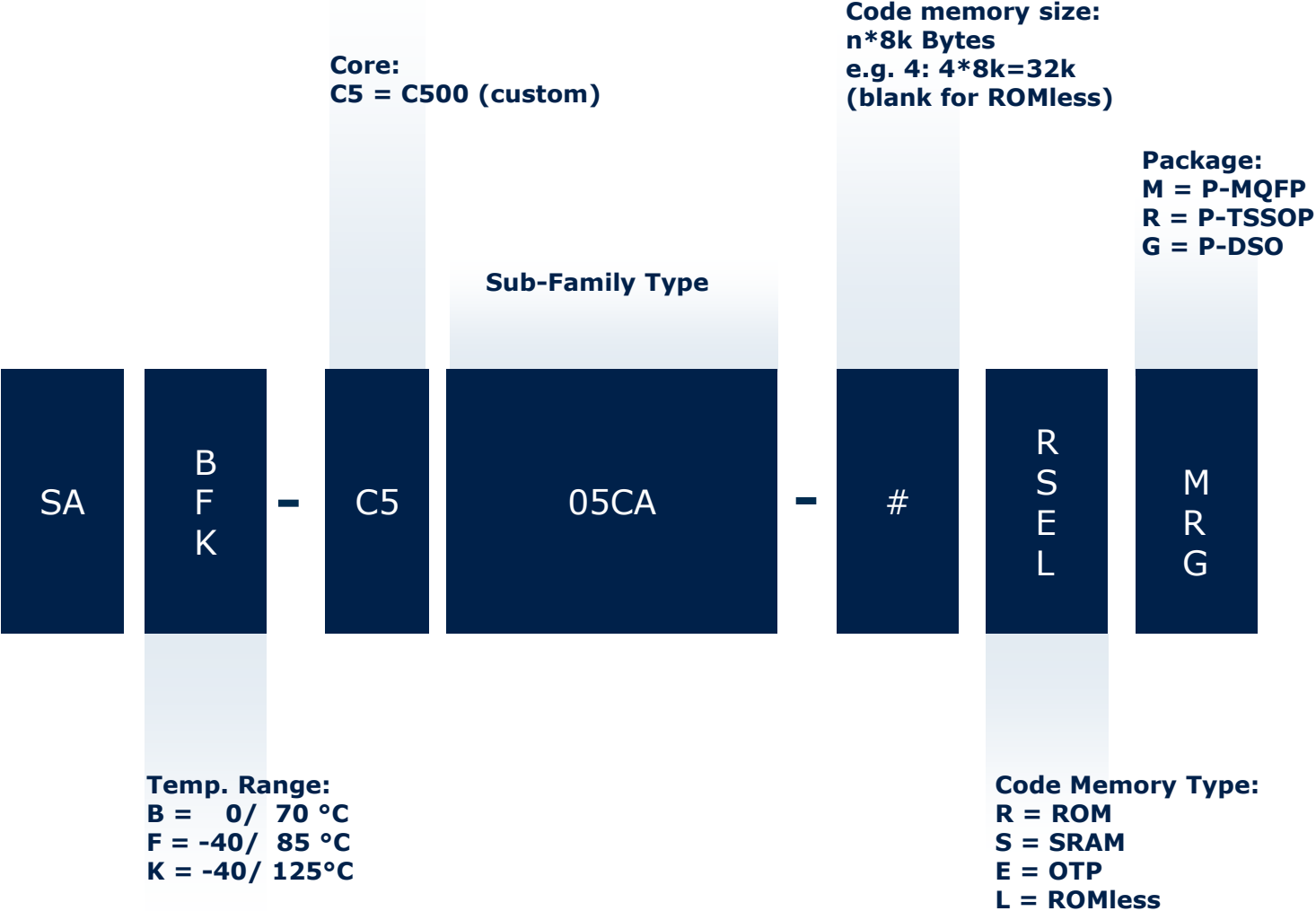
**A = Automotive**  
**I = Industrial**



# Naming Convention for C500 Family



Example: SAB-C505CA-4EM



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